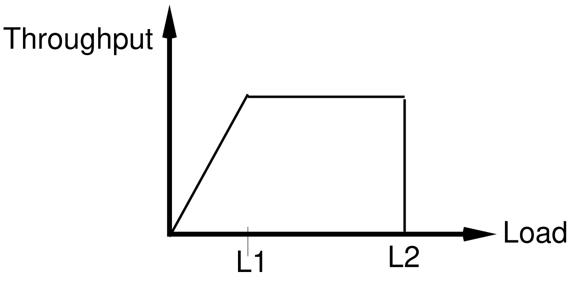
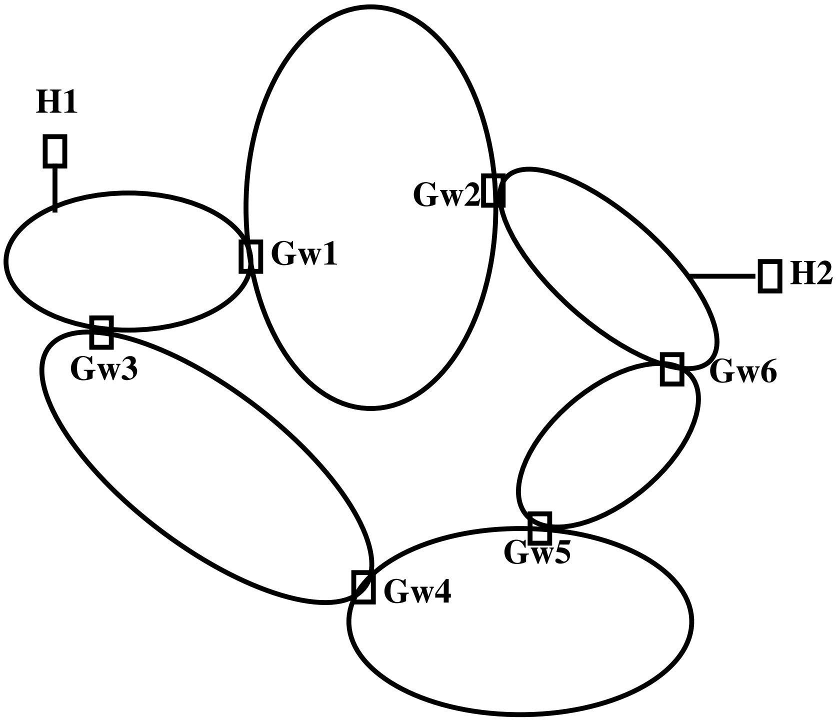

Throughput
Load
L1
L2


H1
Gw1
Gw2
H2
Gw3
Gw6
Gw5
Gw4