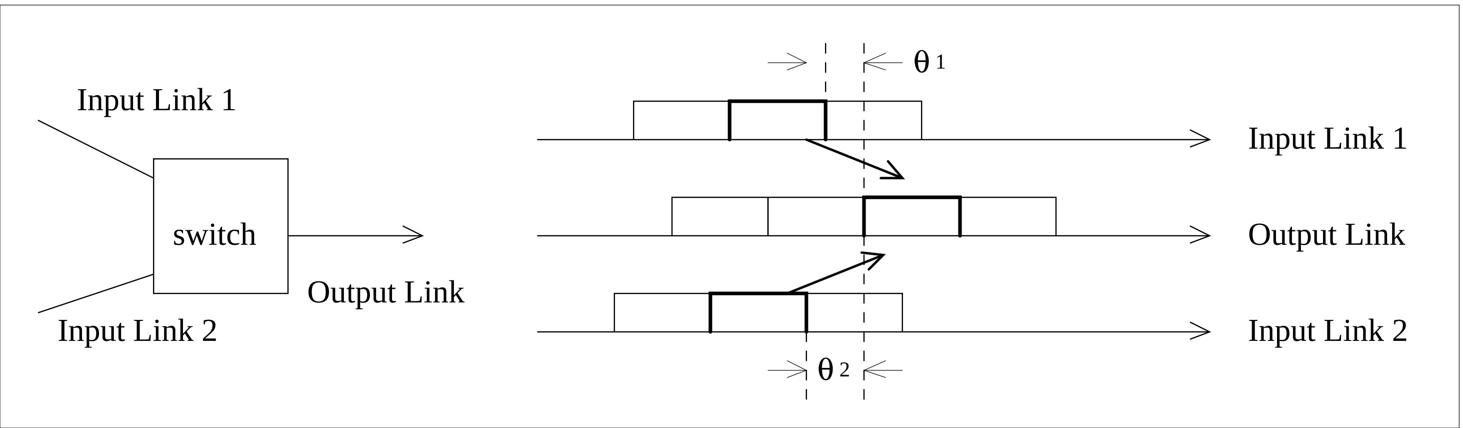
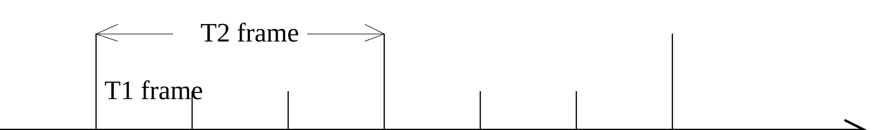
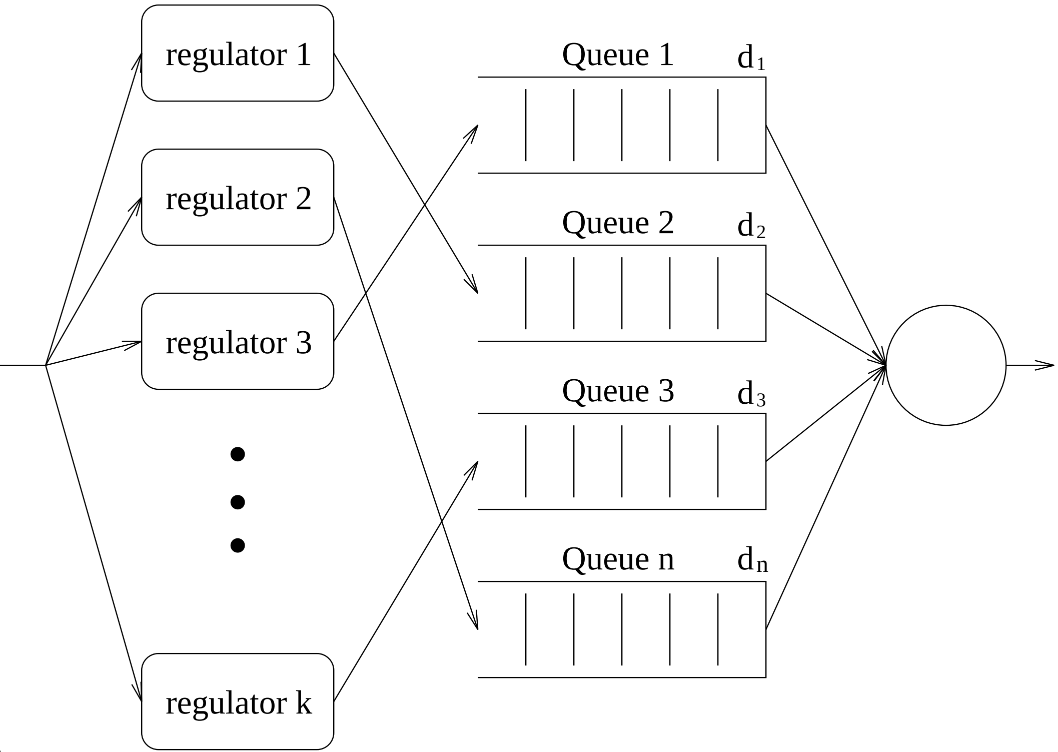
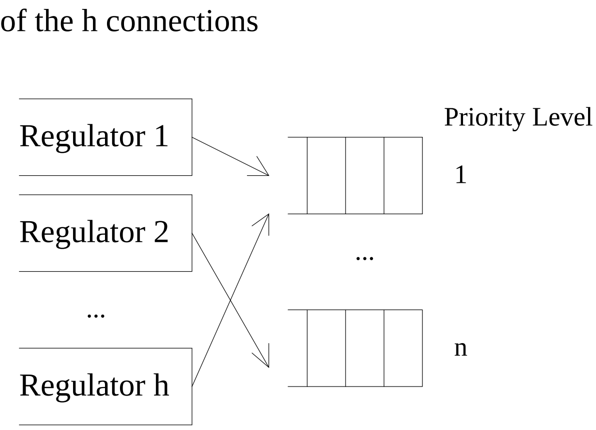
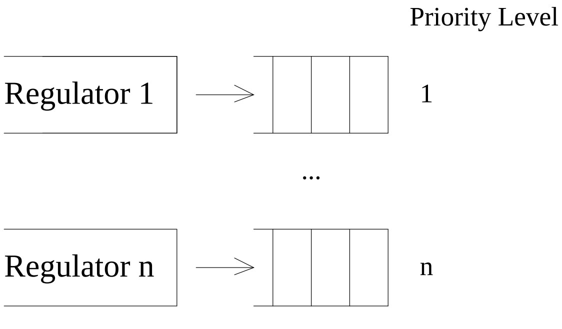

Input Link 1
switch
Output Link
Input Link 2
θ 1
Input Link 1
Output Link
Input Link 2
θ 2


T2 frame
T1 frame


regulator 1
regulator 2
regulator 3
regulator k
Queue 1
d_1
Queue 2
d_2
Queue 3
d_3
Queue n
d_n



RCSP



Stop-and-Go