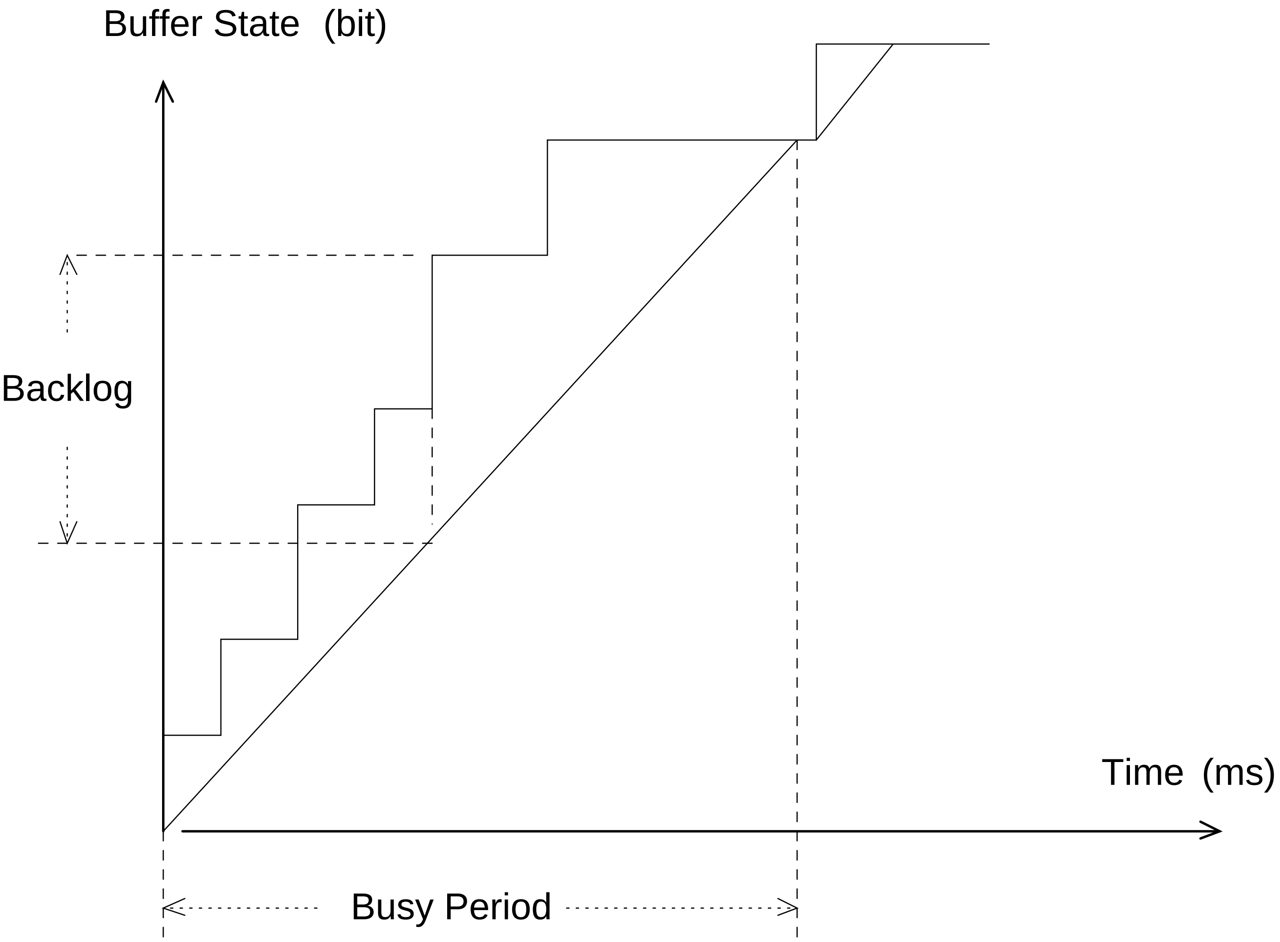
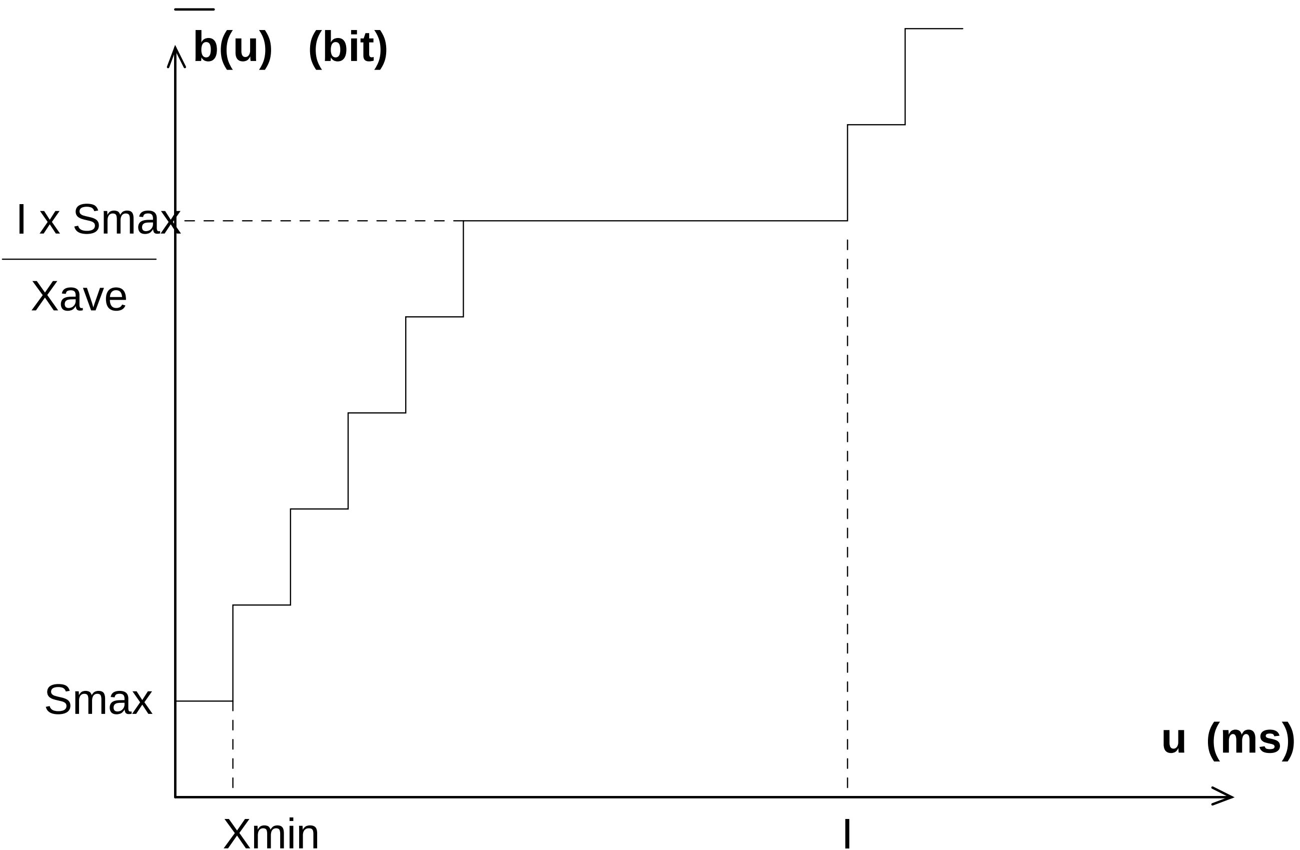
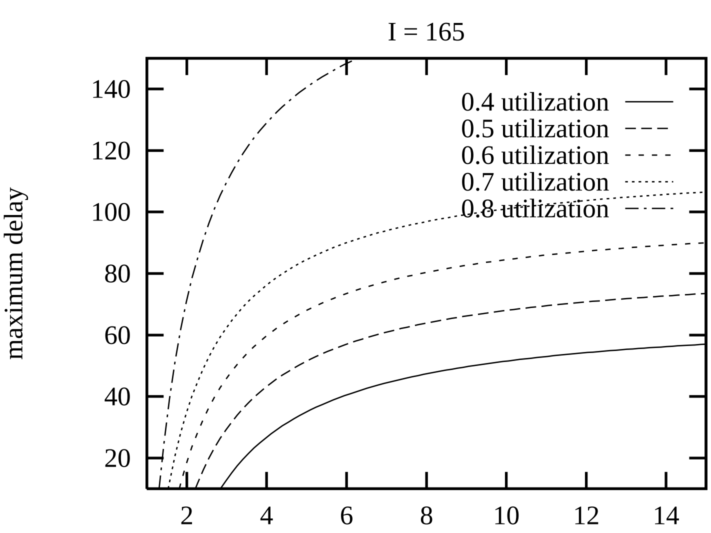
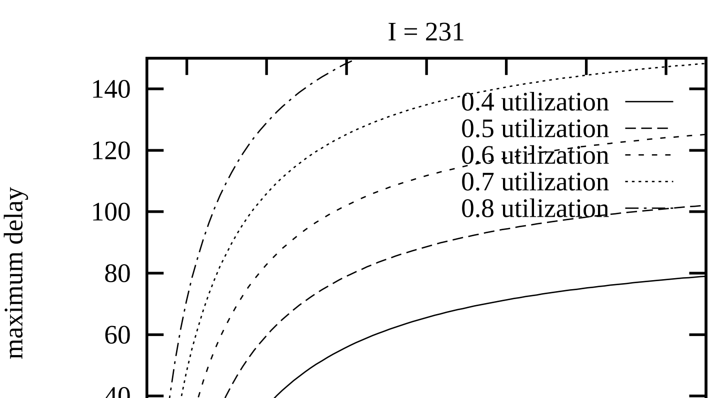
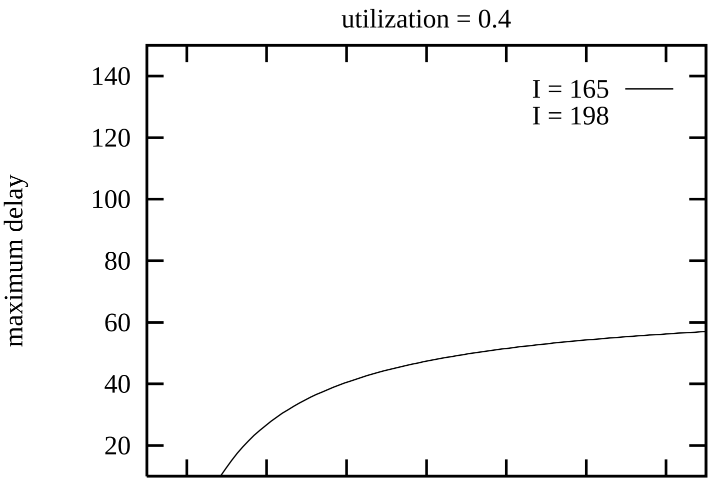

Buffer State (bit)
Backlog
Time (ms)
Busy Period


$\overline{b}(u)$ (bit)
I x Smax
Xave
Smax
u (ms)
Xmin
I


I = 165
maximum delay
Xave/Xmin
0.4 utilization
0.5 utilization
0.6 utilization
0.7 utilization
0.8 utilization
20
40
60
80
100
120
140
2
4
6
8
10
12
14


I = 231
maximum delay
Xave/Xmin
140
120
100
80
60
40
20
2
4
6
8
10
12
14
0.4 utilization
0.5 utilization
0.6 utilization
0.7 utilization
0.8 utilization


utilization = 0.4
I = 165
I = 198
maximum delay
20
40
60
80
100
120
140
2
4
6
8
10
12
14
Xave/Xmin